

AMENDMENT OF THE CLAIMS

1. (Currently Amended) A liquid crystal display, comprising:

a liquid crystal display panel having a liquid crystal cell at each intersection area of gate lines and data lines, which is driven by a frame divided into first and second fields and includes a display area having a specific area and a non-specific area;

an interface part outputting a first data for applying the first data to the display area during the first field;

a video processor outputting a second data for displaying in the specific area and a third data for displaying the non-specific area during the second field, the second data being generated from converting the first data to have different brightness level from the first data;

a memory temporarily storing the second and third data;

a position designator designating the specific area of the liquid crystal display panel where the second data is implemented;

a timing controller realigning the first to third data;

a data driver applying the first data to the display area during the first field, and applying the second data to the specific area and the third data to the non-specific area during the second field; and

a gate driver supplying a scan pulse,

wherein the second data has different brightness level from the third data, and the third data is a black data,

wherein the liquid crystal display panel displays a video signal of the first data in the first field, and then display the second data at the specific area and the third data at the remaining area in the second field,

wherein the brightness level of the second data is higher than brightness level of the first data, [[and]]

wherein the position designator designates the specific area in accordance with a program in a computer system, and

wherein the liquid crystal display is driven at a high speed and, at the same time, in a high picture quality mode, the video signal is applied to the liquid crystal panel at the specific area and the non-specific area in the first field of one frame.

2. (Canceled)

3. (Previously Presented) The liquid crystal display according to claim 1, wherein the memory temporarily stores position data for the specific area.

4. (Original) The liquid crystal display according to claim 1, wherein the video processor is comprised of a multiplexor.

5-6. (Canceled).

7. (Currently Amended) A liquid crystal display, comprising:

a liquid crystal display panel having a liquid crystal cell at each intersection area of gate lines and data lines, which is driven by a frame divided into first and second fields and includes a display area having a specific area and a non-specific area;

a computer for providing a first data and a position data for the specific area of the liquid crystal display panel;

a video processor outputting a second data for displaying in the specific area and a third data for displaying in the non-specific area during the second field, the second data being generated from converting the first data to have different brightness level from the first data;

a memory temporarily storing the second and third data;

a timing controller realigning the first to third data;

a data driver applying the first data to the display area during the first field, and applying the second data to the specific area and the third data to the non-specific area during the second field; and

a gate driver supplying a scan pulse,

wherein the second data has different brightness level from the third data, and the third data is a black data, and

wherein the liquid crystal display panel displays a video signal of the first data in the first field, and then display the second data at the specific area and the third data at the remaining area in the second field,

wherein a frame of image data stored in the memory includes at least two fields, [[and]]

wherein each of the two fields correspond to a different brightness level, and

wherein the liquid crystal display is driven at a high speed and, at the same time, in a high picture quality mode, the video signal is applied to the liquid crystal panel at the specific area and the non-specific area in the first field of one frame.

8. (Previously Presented) The liquid crystal display according to claim 7, wherein the memory temporarily stores position data for the specific area.

9. (Currently Amended) A driving method of a liquid crystal display, which is driven by a frame divided into first and second fields and includes a display area having a specific area and a non-specific area, comprising the steps of:

providing a first data for displaying the display area;

converting the first data into a second data for displaying in the specific area, the second data having different brightness level from the first data;

generating a third data for displaying in the non-specific area;

applying the first data to the display area during the first field; and

applying a second data to the specific area and applying a third data to the non-specific area during the second field,

wherein the second data has different brightness level from the third data, and the third data is a black data,

wherein the liquid crystal display panel displays a video signal of the first data in the first field, and then display the second data at the specific area and the third data at the remaining area in the second field, [[and]]

wherein the brightness level of the second data is higher than brightness level of the first data, and

wherein the liquid crystal display is driven at a high speed and, at the same time, in a high picture quality mode, the video signal is applied to the liquid crystal panel at the specific area and the non-specific area in the first field of one frame.

10. (Previously Presented) The liquid crystal display according to claim 9, wherein the memory temporarily stores position data for the specific area.

11. (Previously Presented) The liquid crystal display according to claim 1, wherein a frame of image data stored in the memory includes at least two fields.

12. (Previously Presented) The liquid crystal display according to claim 11, wherein each of the two fields correspond to a different brightness level.

13. (Canceled).

14. (Previously Presented) The liquid crystal display according to claim 7, wherein the memory is connected between the video processor and the timing controller.

15-17. (Canceled).